

Fig. 2

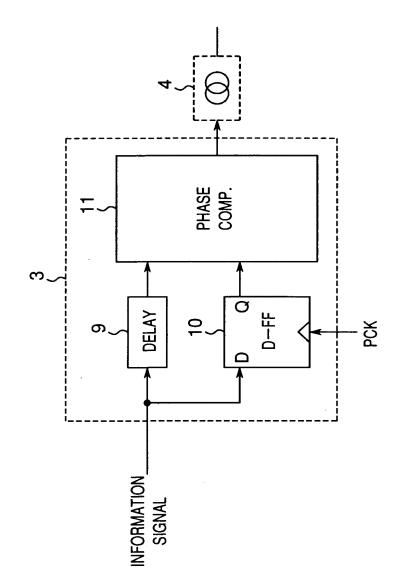


Fig.3A

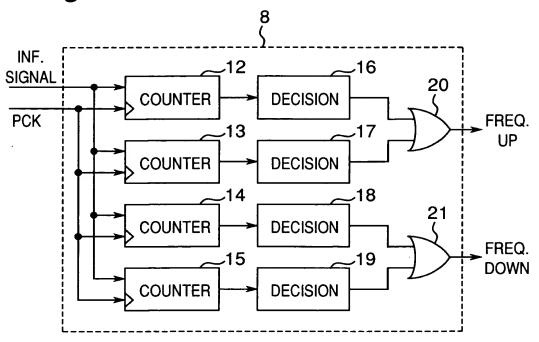


Fig.3B

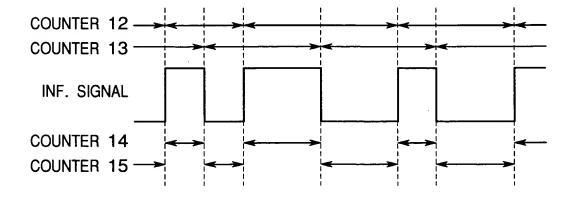


Fig.4

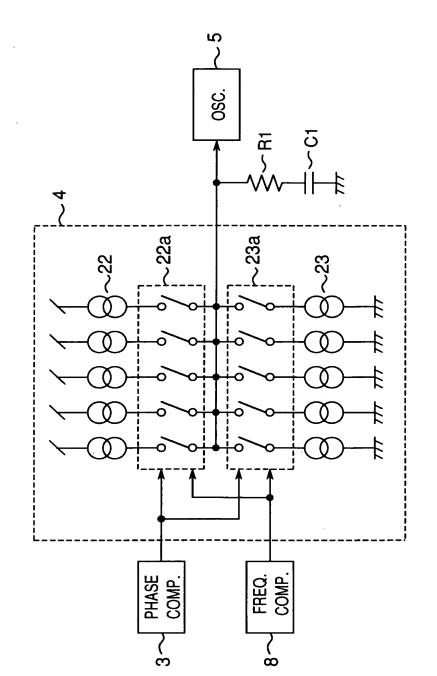


Fig.5

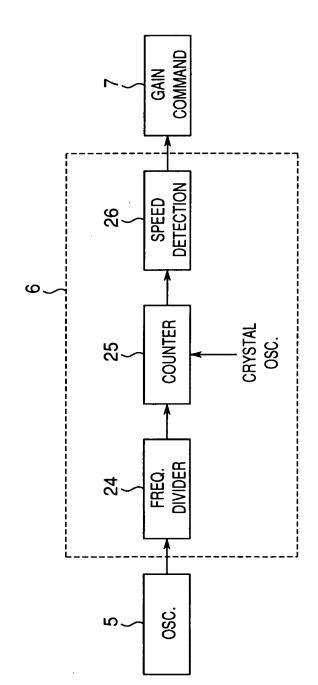


Fig.6

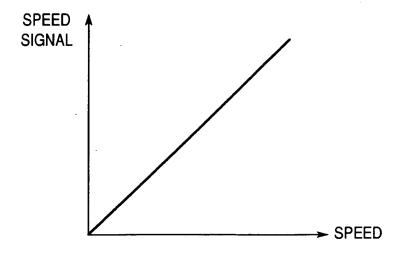


Fig.7

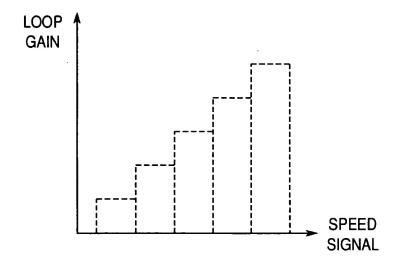
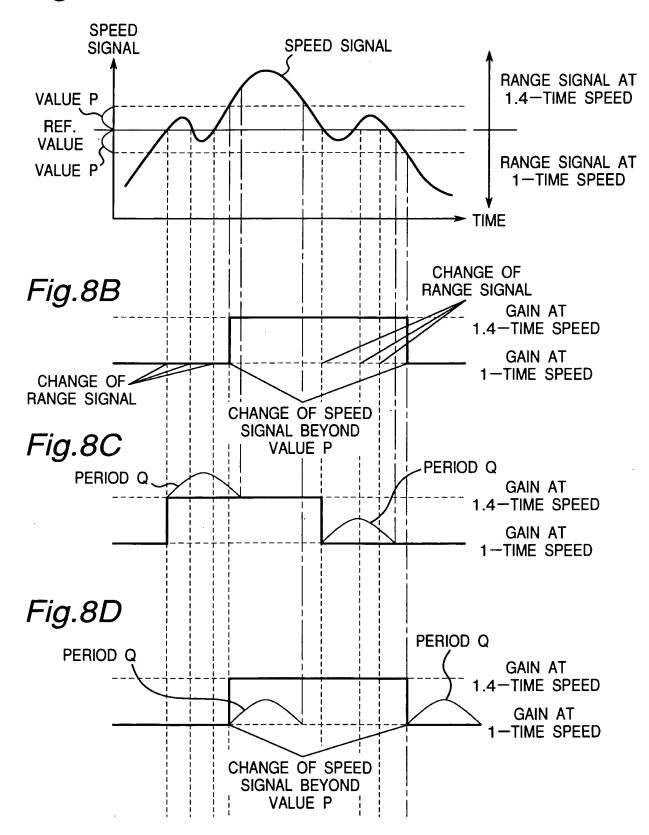


Fig.8A



GAIN ▼ OUTPUT SPEED SENSOR 9 OSC. **5A** 왕 DIGITAL FILTER 27 **▼** MULTIPLIER **4**A Fig.9 PHASE COMP. FREQ. COMP. σ

Fig. 10

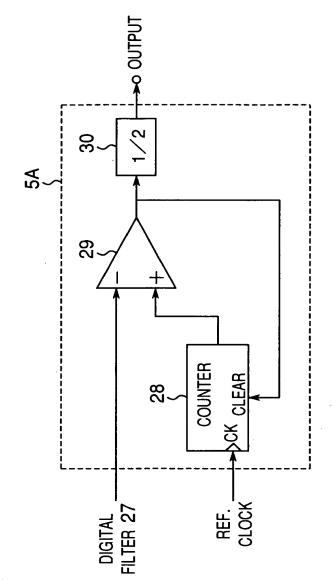


Fig.11 PRIOR ART

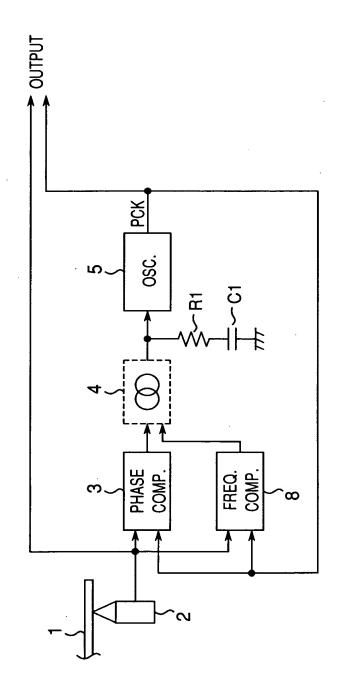


Fig.12 PRIOR ART

